

Notice of Allowability	Application No.	Applicant(s)
	10/039,248	MAURITZ ET AL.
	Examiner	Art Unit
	Ted M. Wang	2611
The MAILING DATE of this communication app All claims being allowable, PROSECUTION ON THE MERITS IS herewith (or previously mailed), a Notice of Allowance (PTOL-88 NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT I of the Office or upon petition by the applicant. See 37 CFR 1.31	S (OR REMAINS) CLOSED in 5) or other appropriate commu RIGHTS. This application is s 13 and MPEP 1308.	this application. If not included nication will be mailed in due course. THIS
1. X This communication is responsive to <u>06/01/2007 amendr</u>	<u>ments</u> .	
2. X The allowed claim(s) is/are 1,5,9-14,16,17 and 20-22.		
<ol> <li>Acknowledgment is made of a claim for foreign priority of a) ☐ All b) ☐ Some* c) ☐ None of the:</li> <li>1. ☐ Certified copies of the priority documents have:</li> <li>2. ☐ Certified copies of the priority documents have:</li> </ol>	ve been received.	
<ol><li>Copies of the certified copies of the priority d</li></ol>	locuments have been received	I in this national stage application from the
International Bureau (PCT Rule 17.2(a)).	•	
* Certified copies not received:		
Applicant has THREE MONTHS FROM THE "MAILING DATE noted below. Failure to timely comply will result in ABANDON THIS THREE-MONTH PERIOD IS NOT EXTENDABLE.		a reply complying with the requirements
<ol> <li>A SUBSTITUTE OATH OR DECLARATION must be sub- INFORMAL PATENT APPLICATION (PTO-152) which gi</li> </ol>		
5. CORRECTED DRAWINGS ( as "replacement sheets") mi	ust be submitted.	
(a) including changes required by the Notice of Draftspe	erson's Patent Drawing Review	( PTO-948) attached
1) 🔲 hereto or 2) 🔲 to Paper No./Mail Date		
(b) ☐ including changes required by the attached Examine Paper No./Mail Date Identifying indicia such as the application number (see 37 CFR)		
each sheet. Replacement sheet(s) should be labeled as such in	the header according to 37 CF	R 1.121(d).
<ol> <li>DEPOSIT OF and/or INFORMATION about the dep attached Examiner's comment regarding REQUIREMENT</li> </ol>		
· · · · · · · · · · · · · · · · · · ·		
Attachment(s)		
1. Notice of References Cited (PTO-892)		formal Patent Application
2. Notice of Draftperson's Patent Drawing Review (PTO-948)		ımmary (PTO-413), Mail Date <u>7/5/07</u>
3. Information Disclosure Statements (PTO/SB/08),	7. 🛭 Examiner's	Amendment/Comment
Paper No./Mail Date  4.	8. 🛭 Examiner's	Statement of Reasons for Allowance
•	9. 🗌 Other	-
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### **DETAILED ACTION**

# Response to Arguments

1. Applicants' amendments and arguments, filed on 06/01/2007, with respect to Claims 1, 5, 9-14, 16, 17 and 20-22 have been fully considered and persuasive. The 35U.S.C.102(e) rejection has been withdrawn.

## **Examiner Amendments**

- 2. An examiner's amendment to the record appears below. Should the changes and/or additions be unacceptable to applicant, an amendment may be filed as provided by 37 CFR 1.312. To ensure consideration of such an amendment, it MUST be submitted no later than the payment of the issue fee.
- 3. Authorization for this examiner's amendment was given in a telephone interview with Applicant's representative Jan Little-Washington on 07/05/2007.
- 4. The application has been amended as follows:

  In the claims:
  - □ Claim 1, line 24, after "the" delete --- second ---.
  - □ Claim 13, line 23, after "the" delete --- second ---.
  - Claim 17, line 1, change "14" to --- 16 ---.
  - □ Claim 20, line 27, after "the" delete --- second ---.

# Allowable Subject Matter

- 5. Claims 1, 5, 9-14, 16, 17 and 20-22 are allowed.
- 6. The following is an examiner's statement of reasons for allowance.

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- The prior art fails to teach an apparatus/system of Claims 1, 13 and 20 that specifically comprises the following:
  - -- The instant application is deemed to be directed to a non-obvious improvement over the admitted prior art of the instant application and the invention patented in Pat. No. 6,996,232. The improvement comprises "wherein the second logic further comprises impedance matching circuitry, to match impedance of the second logic to the differential transmission line, the impedance matching circuitry comprising: an operational amplifier; a first resistance; a second resistance; a first capacitance; a second capacitance; and a transistor, wherein a first terminal of the first resistance, a first terminal of the second resistance, and a first terminal of the first capacitance are coupled to a minus input of the operational amplifier, wherein a second terminal of the first capacitance is coupled to ground, wherein a plus input of the operational amplifier is coupled to Vcc/2, wherein a second terminal of the first resistance, a second terminal of the second capacitance, and a first terminal of the second resistance are coupled to a first terminal of the transistor, wherein a second terminal of the second transistor is coupled to Vcc, wherein an output terminal of the operational amplifier is coupled to a second terminal of the transistor, and wherein a third terminal of the transistor is coupled to ground." as recited.

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7. Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

### Conclusion

- 8. Reference(s) US 6,789,209 and US 6,480,946 are cited because they are put pertinent to the memory system for high speed data transfer. However, none of references teach detailed connection as recited in claim.
- 9. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ted M. Wang whose telephone number is (571) 272-3053. The examiner can normally be reached on M-F, 7:30 AM to 5:00 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Chieh Fan can be reached on 571-272-3042. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at (866) 217-9197 (toll-free).

Ted M Wang

Examiner

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Ted M. Wang